

**Five Days Faculty Development Program**  
on

**RECENT TRENDS IN VLSI DESIGN AND  
EMBEDDED SYSTEMS**

**11 to 15 June 2024**

**REGISTRATION FORM**

- 1.Name:..... Male /Female:.....
- 2.Designation:.....
- 3.Organization:.....
- 4.Teaching Exp:..... Industry Exp:.....
- 5.Highest Qualification:.....
- 6.Specialization:.....
- 7.Areas of Interest:.....
- 8.Address for communication:.....
- 9.Mobile:.....
- 10.E.Mail:.....

**Declaration:**

The information furnished above is true to the best of my knowledge.I agree to abide by the rules and regulations governing the FDP.

Place : \_\_\_\_\_  
Date: \_\_\_\_\_ Signature of the Applicant

**Sponsorship** — \_\_\_\_\_

Mr/Ms/Dr.....is an employee of our institution and is hereby sponsored or attending the above FDP.

Place : \_\_\_\_\_  
Date: \_\_\_\_\_ Signature of the Principal

Note: Please send the scanned copy of the filled and duly signed registration form to the email [subbulakshmi.ece@bharathuniv.ac.in](mailto:subbulakshmi.ece@bharathuniv.ac.in)

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**Co-Coordinator**

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**Dr.S.Varalakshmi, Associate Professor/ECE**

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**Ms.R.Geetha, Assistant Professor/ECE**



**Five days Faculty  
Development Program**

on

**RECENT TRENDS IN VLSI DESIGN  
AND EMBEDDED SYSTEMS**

**Organized by  
Department of Electronics and  
Communication Engineering**

**BHARATH INSTITUTE OF SCIENCE  
AND TECHNOLOGY**

**BHARATH INSTITUTE OF HIGHER  
EDUCATION AND RESEARCH  
CHENNAI-73**

**11-06-2024 to 15-06-2024**

## About the Institution

Bharath institute of Science and Technology (BIST) started with Sri Lakshmi Ammal Education Trust as the first self-financing Engineering College in Tamil Nadu in 1984. The trust then established Sree Balaji Dental College and Hospital in 1989. Bharath Institute of Higher Education and Research (BIHER) is providing multi-stream, multi-cultural and high quality education to nearly 10000 students worldwide. It is functioning in a sprawling area of 603 acres of land with 1.3 crore sq. buildings located in 6 campuses both in Tamil Nadu and Puducherry with the state of the art infrastructure facilities. There are well qualified Teaching Faculty member to teach various UG and PG programmes and to guide.

## About the Department

The department is equipped with state-of-the-art facilities to carry out research work at all levels. The research focus of the department is in the area of power electronics, renewable energy systems, control systems, etc. The department also actively involved in multi-disciplinary research activities. The department has dedicated faculty, support staff, state-of-the-art teaching and research laboratories and is recognized for its excellence in research, teaching and service to the Nation. The department provides healthy & competitive environment for all round development of students leading to several remarkable achievements in GATE, CAT, GRE, TOEFEL, PSUs etc. The faculties and students of EEE & ECE .Departments do present papers in National and International conferences. The department is also having a very good industry institute interaction through Consultancy, Training Program, Collaborative Research work, student's project work etc. The School has MoU with Blue Hora University, Ethiopia, East Africa to share knowledge and experience to our students. The school also has Centre of Excellence in Electric mobility, VLSI Design and RF & Microwave Engineering in order to enhance the skills of faculty and students. Our placements are consistently above 90% for the past three years. Our students are placed in both core industries and software industries. Students are guided for placements right from the first year. We cater to various needs of our students. Students are encouraged to be part of various Student Development Clubs and are encouraged to participate in social activities through NSS. The Electronics and Communication Engineering Department is a big department with over 40 faculty Members. 15 Professors, 10 Associate professors and 15 Assistant Professors are there. It has got well furnished labs and research facilities. It has got VLSI research lab and incubation center.

## Aim of the programme

The aim of this Faculty Development Program (FDP) is to enhance the knowledge and skills of faculty members in the field of Very Large Scale Integration (VLSI) design and embedded systems. This program seeks to:

- Update Knowledge on Cutting-edge Technologies:
- Introduce participants to the latest trends, technologies, and advancements in VLSI design and embedded systems.

- Enhance Teaching and Research Competence: Equip faculty with the necessary tools and methodologies to improve their teaching and research capabilities in these specialized areas.
- Promote Innovation and Research: Encourage innovative thinking and research initiatives among faculty members to contribute to advancements in VLSI and embedded systems.
- Foster Industry-Academia Collaboration: Strengthen the collaboration between academic institutions and the industry to align academic programs with industry needs and promote practical learning experiences.
- Hands-on Training and Workshops: Provide hands-on sessions and workshops on various VLSI design tools, simulation techniques, and embedded system development to enhance practical skills.

By the end of the program, participants should have a comprehensive understanding of current trends and future directions in VLSI design and embedded systems, enabling them to deliver updated and relevant knowledge to their students and contribute effectively to research in these areas.

## Indicative Topics

- Memory Technologies in VLSI Trends towards AI
- Role of VLSI in IoT and Edge Computing
- Hardware Modeling using Verilog
- Recent trends in Embedded system design.
- VLSI Implementation for Reconfigurable systems.
- The Vital Role of Integrated Circuit Testing
- Recent Trends in VLSI design.
- Hands on Training in Embedded systems and IoT
- Printing Techniques for Electronics Applications

## Resource Person

**Dr. V. JEYALAKSHMI,**  
Professor, Department of ECE, CEG Campus  
Anna University, Chennai.  
**Dr. M. SELVI,**  
Associate Professor,  
School of Computer Science and Engineering, VIT, Vellore.

**Dr. A. GANESH RAM,**  
Assistant Professor,  
Department of Instrumentation Engineering,  
MIT Campus, Anna University, Chennai.  
**Dr. S. MEYYAPPAN,**  
Assistant Professor,  
Department of Instrumentation Engineering, MIT Campus,  
Anna University, Chennai.  
**Dr. P. NIRMAL KUMAR,**  
Professor, Department of ECE,  
CEG Campus, Anna university, Chennai.  
**Dr. G. SUDHAGAR,**  
Associate Professor, ECE  
BIHER  
**Dr. P. DHINAKAR,**  
Professor, ECE  
BIHER  
**Mr. V. KALAYANA RAMAN,**  
Business development Manager,  
Enthu Technology solutions, India Pvt Ltd, Coimbatore.  
**Dr. E. MANIKANDAN,**  
Assistant Professor,  
Senior Grade,  
School of Electronics Engineering,  
VIT, Chennai.

## Registration

**Registration Deadline : 10.06.2024**  
**Link : <https://forms.gle/PodP7MJUwcLZCnBA9>**  
**Registration Fee : Rs.500/-**  
**Account details:**  
**Name of Account: School of Electrical Engineering**  
**A/C Number: 4557002100003104**  
**Bank Name: PUNJAB NATIONAL BANK**  
**Branch Name: BIHER, SELAIYUR**  
**Branch IFSC Code : PUNB0455700**  
**Kindly register for this program and circulate the brochure among your known contacts for active participation.**

## Address for communication

**The Co-Ordinator**  
**FDP**  
**Department of Electronics and Communication Engineering**  
**Bharath Institute of Science and Technology**  
**Bharath Institute of Higher Education and Research**  
**#173 Agharam Road Selaiyur, Chennai- 600 073 Tamil Nadu, India**  
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